

Fabrication and Characterization of Overlap Josephson Junctions and CPW Resonators for the Development of Superconducting Quantum Network

Muhammad Shoaib¹, Valentina Di Meo², Carlo Barone^{4,5,6}, Davide Buono³, Giovanni Carapella^{4,5}, Alessandro Mauro^{4,5}, Sergio Pagano^{4,5,6}, Berardo Ruggiero³, Mariagorizia Pia Trippa⁴, Mikhail Lisitskiy², Paolo Silvestrini¹

1. University of Campania "Luigi Vanvitelli", Department of Mathematics and Physics, 81100 Caserta, Italy

2. Institute for Superconductors, Innovative materials, and devices - SPIN, CNR, Pozzuoli, Italy

3. Institute of Applied Sciences and Intelligent Systems-ISASI, CNR, 80078 Pozzuoli, Italy

4. Dipartimento di Fisica "E.R. Caianiello", Via Giovanni Paolo II, 132, 84084 Fisciano (SA), Italy

5. Gruppo Collegato Salerno, Istituto Nazionale di Fisica Nucleare, Via Giovanni Paolo II, 132, 84084 Fisciano (SA), Italy

6 Institute for Superconductors, Innovative materials, and devices - SPIN, CNR, Salerno, Italy

Abstract: We develop a fully complementary metal-oxide semiconductor (CMOS) fabrication technique for realization of a superconducting qubit network (SQN).

Up to now, the Al double-angle evaporation and lift-off technique is widely used for the fabrication of a superconducting qubit with long coherence time, suitable for the realization of quantum simulations and quantum processors with an intermediate scale number of qubits [1]. Several drawbacks of double-angle technology, such as qubit excitation-energy inhomogeneity and low reproducibility on large-diameter wafers, make it unsuitable for realizing a large-scale practical computer, necessitating the development of an alternative technology [2]. We develop a fully complementary metal-oxide-semiconductor (CMOS)-based fabrication technique [3] to realize a superconducting qubit network (SQN). In our current research, we develop a planar single-angle overlap fabrication process [4] for Al/AIOx/Al Josephson junctions on a Si (100) substrate. To increase the fabrication yield, we proposed a combined approach that included both gentle wet etching to define the base electrode (BE) and radio frequency (RF) Ar cleaning of the BE before oxidation at low pressure for a long time. We comparatively investigated the lift-off process and the wet etching technique for BE patterning. The wet etching process forms the BE edges without residual photoresist, thereby improving fabrication yield. Long-term RF Ar plasma cleaning of the BE improves its smoothness, enabling the formation of a thinner AlOx tunnel barrier and increasing the Josephson critical current density.

Superconducting microwave Coplanar waveguide (CPW) resonators are essential components of modern qubits (Transmon) in the framework of circuit quantum electrodynamics (cQED) [5]. A high-quality-factor, low-microwave-loss CPW resonator is demanded for the readout and manipulation of superconducting qubits during dispersive measurements [6]. As for the material research on CPW resonators to maximize their internal Q_i quality factor, CPW resonators with different geometries are designed for resonant frequencies ranging from 4 GHz to 12 GHz in 0.5 GHz steps. Sapphire and Si as substrates and tantalum, niobium, and aluminum as superconducting materials have been investigated for CPW resonators. Preliminary results for Tantalum CPW resonators on C-plane sapphire with niobium buffer layer were fabricated and tested in terms of measurements of scattering parameters $|S_{21}|$ as a function of frequency. From the experimental data, we extract an internal quality factor Q_i of 10^6 using the fitting procedure.

References

- [1] Bankson, S. M., Tillman, C. C., Sellers, J. A., & Hamilton, M. C. "Al/AIOx/Al Josephson Junctions Fabricated by Shadow Evaporation Employing Multiple Symmetric Angled Depositions Per Channel", *IEEE Transactions on Applied Superconductivity*, **33**, (2023).
- [2] Moskalev, D. O., Zikiy, E. V., Pishchimova, A. A., Ezenkova, D. A., Smirnov, N. S., Ivanov, A. I., ... & Rodionov, I. A. "Optimization of shadow evaporation and oxidation for reproducible quantum Josephson junction circuits", in *Scientific reports*, **13**, (2023).
- [3] Van Damme, J., et al. "Advanced CMOS manufacturing of superconducting qubits on 300 mm wafers." *Nature*, **634**, pp. 74-79, (2024).
- [4] Muhammad Shoaib, Valentina Di Meo, Carlo Barone, Davide Buono, Alessandro Mauro, Sergio Pagano, Berardo Ruggiero, Mikhail Lisitskiy, Paolo Silvestrini, "Improvements of the Single Angle Overlap Josephson Junction Technology for Qubit Application" *IEEE Trans. on Appl. Supercond.*, EUCAS2025-3-EP-QB3.30.R1 (2026, in press)
- [5] Foshat, P., Baity, P., Danilin, S., Seferai, V., Poorgholam-Khanjari, S., Feng, H., ... & Delfanzari, K. "Characterizing niobium nitride-based superconducting coplanar waveguide resonators for microwave hybrid circuit quantum electrodynamics," *IEEE Transactions on Applied Superconductivity*, **35**, pp. 1-16, (2025).
- [6] Poorgholam-Khanjari, S., Seferai, V., Foshat, P., Rose, C., Feng, H., Hadfield, R. H., ... & Delfanzari, K. "Engineering high-Q superconducting tantalum microwave coplanar waveguide resonators for compact coherent quantum circuit," *Scientific Reports*, **15**, pp. 27113, (2025).